

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	29	(gustavson.in. and fred.in.) or (gunnels.in. and john.in.)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/12 16:25
L2	26	1 and (matrix or matrices)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/12 16:25
L3	7	2 and triangular	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/12 16:25
L4	29	("4594682" "4685076" "4722049" "4760545" "4858115" "4873630" "4881168" "4888679" "4888682" "4926317" "4958274" "5019969" "5032985" "5091848" "5111389" "5133059" "5133061" "5134697" "5148536" "5179674" "5206822" "5237685" "5265237" "5513366").PN. OR ("5680338"). URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/12/12 16:36
L5	9	(hybrid adj (full adj pack\$2))	US-PGPUB; USPAT; USOCR	OR	ON	2006/12/12 16:39
L6	12961	(matrix or matrices) and triangular\$1 and (rectangular or square\$1) and @ad<"20030929"	US-PGPUB; USPAT; USOCR	OR	ON	2006/12/12 16:40
L7	13	((("5099447") or ("7028168") or ("6470368") or ("20030088600") or ("6021420") or ("5644517") or ("7031994")).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/12 16:43
L9	1624	6 and hybrid	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/12 16:43

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L10	1055	9 and (convert\$3 or conversion) and format\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/12 16:44
L11	7	10 and "708"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/12 16:46
L12	921	708/200-206.ccls. and @ad<"20030929"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/12 16:47
L13	146	12 and (matrix or matrices)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/12 16:47
L14	9	13 and triangular	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/12 16:50
L15	0	("5099447.pn.").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/12 16:51
L16	2	"5099447".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/12 16:51
L17	10	("3617720" "4594682" "4697247" "4719588" "4841469" "4914615").PN. OR ("5099447").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/12/12 17:12
L18	0	lapack and 10	US-PGPUB; USPAT; USOCR	OR	ON	2006/12/12 17:13

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L19	5	lapack and 6	US-PGPUB; USPAT; USOCR	OR	ON	2006/12/12 17:13
L20	5	("5206822" "5392429" "5655137" "6088523" "6694343").PN. OR ("7089275").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/12/12 17:14
L21	11	("4787057" "5200915" "5301342" "5905666" "5983230" "6446105" "6470368" "6601080").PN. OR ("6694343"). URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/12/12 17:17
L22	37	6 and (blas or (basic adj linear algebra adj subroutine))	US-PGPUB; USPAT; USOCR	OR	ON	2006/12/12 17:18

Inventor Information for 10/671933

Inventor Name	City	State/Country
GUSTAVSON, FRED GEHRUNG	BRIARCLIFF MANOR	NEW YORK
GUNNELS, JOHN A.	MT. KISCO	NEW YORK

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09918814</u>	6842765	150	07/31/2001	PROCESSOR DESIGN FOR EXTENDED-PRECISION ARITHMETIC	GUSTAVSON, FRED G.
<u>08217533</u>	Not Issued	161	03/24/1994	NUMERICALLY INTENSIVE COMPUTER ACCELERATOR	GUSTAVSON, FRED G.
<u>08313970</u>	5758176	250	09/28/1994	METHOD AND SYSTEM FOR PROVIDING A SINGLE-INSTRUCTION, MULTIPLE-DATA EXECUTION UNIT FOR PERFORMING SINGLE-INSTRUCTION, MULTIPLE-DATA OPERATIONS WITHIN A SUPERSCALAR DATA PROCESSING SYSTEM	GUSTAVSON, FRED G.
<u>08313971</u>	5513366	150	09/28/1994	METHOD AND SYSTEM FOR DYNAMICALLY RECONFIGURING A REGISTER FILE IN A VECTOR PROCESSOR	GUSTAVSON, FRED G.
<u>08354402</u>	5563818	150	12/12/1994	A METHOD AND SYSTEM FOR PERFORMING FLOATING-POINT DIVISION USING SELECTED APPROXIMATION VALUES	GUSTAVSON, FRED G.
<u>08368171</u>	Not Issued	166	01/04/1995	METHOD AND SYSTEM FOR ADDRESSING REGISTERS IN A DATA PROCESSING UNIT IN AN INDEXED ADDRESSING MODE	GUSTAVSON, FRED G.
<u>08368172</u>	5680338	250	01/04/1995	METHOD AND SYSTEM FOR VECTOR PROCESSING UTILIZING SELECTED VECTOR ELEMENTS	GUSTAVSON, FRED G.
<u>08368173</u>	5887183	150	01/04/1995	METHOD AND SYSTEM IN A DATA PROCESSING SYSTEM FOR LOADING AND STORING VECTORS IN A PLURALITY OF MODES	GUSTAVSON, FRED G.
<u>08368176</u>	5890222	150	01/04/1995	METHOD AND SYSTEM FOR ADDRESSING REGISTERS IN A DATA PROCESSING UNIT IN AN INDIRECT ADDRESSING MODE	GUSTAVSON, FRED G.
<u>08589770</u>	5751619	250	01/22/1996	RECURRENT ARITHMETICAL COMPUTATION USING CARRY-SAVE ARITHMETIC	GUSTAVSON, FRED G.

<u>08619456</u>	<u>5825677</u>	250	03/20/1996	NUMERICALLY INTENSIVE COMPUTER ACCELERATOR	GUSTAVSON, FRED G.
<u>08687825</u>	<u>5832533</u>	250	07/26/1996	METHOD AND SYSTEM FOR ADDRESSING REGISTERS IN A DATA PROCESSING UNIT IN AN INDEXED ADDRESSING MODE	GUSTAVSON, FRED G.
<u>10671887</u>	Not Issued	71	09/29/2003	Method and structure for producing high performance linear algebra routines using composite blocking based on L1 cache size	GUSTAVSON, FRED GEHRUNG
<u>10671888</u>	Not Issued	41	09/29/2003	Method and structure for producing high performance linear algebra routines using register block data format routines	GUSTAVSON, FRED GEHRUNG
<u>10671889</u>	Not Issued	25	09/29/2003	Method and structure for producing high performance linear algebra routines using level 3 prefetching for kernel routines	GUSTAVSON, FRED GEHRUNG
<u>10671933</u>	Not Issued	30	09/29/2003	Method and structure for producing high performance linear algebra routines using a hybrid full-packed storage format	GUSTAVSON, FRED GEHRUNG
<u>10671934</u>	Not Issued	30	09/29/2003	Method and structure for producing high performance linear algebra routines using streaming	GUSTAVSON, FRED GEHRUNG
<u>10671935</u>	Not Issued	25	09/29/2003	Method and structure for producing high performance linear algebra routines using a selectable one of six possible level 3 L1 kernel routines	GUSTAVSON, FRED GEHRUNG
<u>10671937</u>	Not Issued	25	09/29/2003	Method and structure for producing high performance linear algebra routines using preloading of floating point registers	GUSTAVSON, FRED GEHRUNG
<u>11035902</u>	Not Issued	25	01/14/2005	Method and structure for a generalized cache-register file interface with data restructuring methods for multiple cache levels and hardware pre-fetching	GUSTAVSON, FRED GEHRUNG
<u>11035933</u>	Not Issued	30	01/14/2005	Method and structure for cache aware transposition via rectangular subsections	GUSTAVSON, FRED GEHRUNG
<u>11039907</u>	Not Issued	30	01/24/2005	Method and structure for algorithmic overlap in parallel processing for exploitation when load imbalance is dynamic and predictable	GUSTAVSON, FRED GEHRUNG
<u>11045354</u>	Not Issued	25	01/31/2005	Method and structure for a hybrid full- packed storage format as a single rectangular format data structure	GUSTAVSON, FRED GEHRUNG
<u>11050945</u>	Not Issued	30	02/07/2005	System and method for detecting a faulty object in a system	GUSTAVSON, FRED GEHRUNG
<u>11052877</u>	Not Issued	30	02/09/2005	System and method for algorithmic cache- bypass	GUSTAVSON, FRED GEHRUNG
<u>11133254</u>	Not Issued	25	05/20/2005	Method and structure for improving processing efficiency in parallel processing machines for rectangular and triangular	GUSTAVSON, FRED GEHRUNG

				matrix routines	
<u>11328344</u>	Not Issued	30	01/09/2006	Method and structure for an improved data reformatting procedure	GUSTAVSON, FRED GEHRUNG

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Application#	Patent#	Status	Date Filed	Title	Inventor Name
10671887	Not Issued	71	09/29/2003	Method and structure for producing high performance linear algebra routines using composite blocking based on L1 cache size	GUNNELS, JOHN A.
10671888	Not Issued	41	09/29/2003	Method and structure for producing high performance linear algebra routines using register block data format routines	GUNNELS, JOHN A.
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10671937	Not Issued	25	09/29/2003	Method and structure for producing high performance linear algebra routines using preloading of floating point registers	GUNNELS, JOHN A.
11035902	Not Issued	25	01/14/2005	Method and structure for a generalized cache-register file interface with data restructuring methods for multiple cache levels and hardware pre-fetching	GUNNELS, JOHN A.
11035933	Not Issued	30	01/14/2005	Method and structure for cache aware transposition via rectangular subsections	GUNNELS, JOHN A.
11039907	Not Issued	30	01/24/2005	Method and structure for algorithmic overlap in parallel processing for exploitation when load imbalance is dynamic and predictable	GUNNELS, JOHN A.
11041935	Not Issued	30	01/26/2005	Method and structure for high-performance linear algebra in the presence of limited outstanding miss slots	GUNNELS, JOHN A.
11045354	Not Issued	25	01/31/2005	Method and structure for a hybrid full-packed storage format as a single	GUNNELS, JOHN A.

				rectangular format data structure	
11050945	Not Issued	30	02/07/2005	System and method for detecting a faulty object in a system	GUNNELS, JOHN A.
11052216	Not Issued	30	02/08/2005	Method and structure for skewed block-cyclic distribution of lower-dimensional data arrays in higher-dimensional processor grids	GUNNELS, JOHN A.
11052877	Not Issued	30	02/09/2005	System and method for algorithmic cache-bypass	GUNNELS, JOHN A.
11133254	Not Issued	25	05/20/2005	Method and structure for improving processing efficiency in parallel processing machines for rectangular and triangular matrix routines	GUNNELS, JOHN A.
11328344	Not Issued	30	01/09/2006	Method and structure for an improved data reformatting procedure	GUNNELS, JOHN A.
60377710	Not Issued	159	05/03/2002	System and method for performing linear algebra	GUNNELS, JOHN A.

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1. **PLAPACK Parallel Linear Algebra Package Design Overview**
Alpatov, P.; Baker, G.; Edwards, H.C.; Gunnels, J.; Morrow, G.; Overfelt, J.; van de Geijn, R.;
[Supercomputing, ACM/IEEE 1997 Conference](#)
15-21 Nov. 1997 Page(s):29 - 29
Digital Object Identifier 10.1109/SC.1997.10004
[AbstractPlus](#) | Full Text: [PDF\(728 KB\)](#) IEEE CNF
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2. **An Overview of the BlueGene/L Supercomputer**
Adiga, N.R.; Almasi, G.; Almasi, G.S.; Aridor, Y.; Barik, R.; Beece, D.; Bellofatto, R.; Bhanot, G.; Bickford, R.; Blumrich, M.; Bright, A.A.; Brunheroto, J.; Cascaval, C.; Castanos, J.; Chan, W.; Ceze, L.; Coteus, P.; Chatterjee, S.; Chen, D.; Chiu, G.; Cipolla, T.M.; Crumley, P.; Desai, K.M.; Deutsch, A.; Domany, T.; Dombrowa, M.B.; Donath, W.; Eleftheriou, M.; Erway, C.; Esch, J.; Fitch, B.; Gagliano, J.; Gara, A.; Garg, R.; Germain, R.; Giampapa, M.E.; Gopalsamy, B.; Gunnels, J.; Gupta, M.; Gustavson, F.; Hall, S.; Haring, R.A.; Heidel, D.; Heidelberger, P.; Herger, L.M.; Hoenicke, D.; Jackson, R.D.; Jamal-Eddine, T.; Kopcsay, G.V.; Krevat, E.; Kurhekar, M.P.; Lanzetta, A.P.; Lieber, D.; Liu, L.K.; Lu, M.; Mendell, M.; Misra, A.; Moatti, Y.; Mok, L.; Moreira, J.E.; Nathanson, B.J.; Newton, M.; Ohmacht, M.; Oliner, A.; Pandit, V.; Pudota, R.B.; Rand, R.; Regan, R.; Rubin, B.; Ruehli, A.; Rus, S.; Sahoo, R.K.; Sanomiya, A.; Schenfeld, E.; Sharma, M.; Shmueli, E.; Singh, S.; Song, P.; Srinivasan, V.; Steinmacher-Burow, B.D.; Strauss, K.; Surovic, C.; Swetz, R.; Takken, T.; Tremaine, R.B.; Tsao, M.; Umamaheshwaran, A.R.; Verma, P.; Vranas, P.; Ward, T.J.C.; Wazlowski, M.; Barrett, W.; Engel, C.; Drehmel, B.; Hilgart, B.; Hill, D.; Kasemkhani, F.; Krolak, D.; Li, C.T.; Liebsch, T.; Marcella, J.; Muff, A.; Okomo, A.; Rouse, M.; Schram, A.; Tubbs, M.; Ulsh, G.; Wait, C.; Wittrup, J.; Bae, M.; Dockser, K.; Kissel, L.; Seager, M.K.; Vetter, J.S.; Yates, K.;
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3. **Large-Scale First-Principles Molecular Dynamics simulations on the BlueGene/L Platform using the Qbox code**
Gygi, F.; Yates, R.K.; Lorenz, J.; Draeger, E.W.; Franchetti, F.; Ueberhuber, C.W.; de Supinski, B.R.; Kral, S.; Gunnels, J.A.; Sexton, J.C.;
[Supercomputing, 2005. Proceedings of the ACM/IEEE SC 2005 Conference](#)
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4. **A high-performance SIMD floating point unit for BlueGene/L: architecture, compilation, and algorithm design**
Bachega, L.; Siddhartha Chatterjee; Dockser, K.A.; Gunnels, J.A.; Manish Gupta; Gustavson,

F.G.; Lapkowski, C.A.; Liu, G.K.; Mendell, M.P.; Wait, C.D.; Ward, T.J.C.;
Parallel Architecture and Compilation Techniques, 2004. PACT 2004. Proceedings. 13th International Conference on
29 Sept.-3 Oct. 2004 Page(s):85 - 96
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5. **Unlocking the Performance of the BlueGene/L Supercomputer**

Almasi, G.; Chatterjee, S.; Gara, A.; Gunnels, J.; Gupta, M.; Henning, A.; Moreira, J.E.; Walkup, B.;
Supercomputing, 2004. Proceedings of the ACM/IEEE SC2004 Conference
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6. **Fault-tolerant high-performance matrix multiplication: theory and practice**

Gunnels, J.A.; Katz, D.S.; Quintana-Orti, E.S.; Van de Geijn, R.A.;
Dependable Systems and Networks, 2001. Proceedings. The International Conference on
1-4 July 2001 Page(s):47 - 56
Digital Object Identifier 10.1109/DSN.2001.941390
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7. **PLAPACK: high performance through high-level abstraction**

Baker, G.; Gunnels, J.; Morrow, G.; Riviere, B.; van de Geijn, R.;
Parallel Processing, 1998. Proceedings. 1998 International Conference on
10-14 Aug. 1998 Page(s):414 - 422
Digital Object Identifier 10.1109/ICPP.1998.708513
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8. **A flexible class of parallel matrix multiplication algorithms**

Gunnels, J.; Lin, C.; Morrow, G.; van de Geijn, R.;
Parallel Processing Symposium, 1998. 1998 IPPS/SPDP. Proceedings of the First Merged International...and Symposium on Parallel and Distributed Processing 1998
30 March-3 April 1998 Page(s):110 - 116
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9. **Genetic algorithms and simulated annealing for gene mapping**

Gunnels, J.; Cull, P.; Holloway, J.L.;
Evolutionary Computation, 1994. IEEE World Congress on Computational Intelligence.. Proceedings of the First IEEE Conference on
27-29 June 1994 Page(s):385 - 390 vol.1
Digital Object Identifier 10.1109/ICEC.1994.349920
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[Parallel Architecture and Compilation Techniques, 2004. PACT 2004. Proceedings. 13th International Conference on](#)
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- ☐ **3. Fault-tolerant high-performance matrix multiplication: theory and practice**
 Gunnels, J.A.; Katz, D.S.; Quintana-Orti, E.S.; Van de Gejin, R.A.;
[Dependable Systems and Networks, 2001. Proceedings. The International Conference on](#)
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